Arjun Ramesh

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RESEARCH STATEMENT

My research interests encompass **software virtualization** and **debugging** with a strong focus on applications targetting cyber-physical edge systems. With a comprehensive systems background – OS, embedded, compilers, architecture – I am dedicated to enabling robust, usable, and performant software ecosystem design at the edge.

EDUCATION

Carnegie Mellon University — VMs, Compilers, Distributed/Edge Computing, OS, Networking, CV — *Aug 2021 - Present*
PhD+MS, Electrical & Computer Engineering — *GPA: 3.87*

The University of Texas at Austin — Comp. Arch., Algorithms, Embedded, RTOS, VLSI, HW/SW Parallelism — *Aug 2017-2021*
BS, Electrical & Computer Engineering — *GPA: 4.00*

PUBLICATIONS

Empowering WebAssembly With Thin-Kernel Interfaces — *Under Review (EuroSys '25)*
A. Ramesh, T. Huang, B. Titzer, A. Rowe — *Virtualization, OS*

Unveiling Heisenbugs with Diversified Execution — *Minor Revision (OOPSLA '25)*
A. Ramesh, T. Huang, J. Riar, B. Titzer, A. Rowe — *SW Testing, Edge Systems*

Bringing Runtime Prediction up to Speed for the Edge — *Under Review (MLSYS '25)*
T. Huang, *A. Ramesh*, E. Ruppel, N. Pereira, A. Rowe, C. Joe-Wong — *ML, Edge Systems*

A Framework for Orchestration of Edge-Cloud Distributed Systems — *RTAS '25*
E. Ruppell et. al (including *A. Ramesh*) — *Distributed, Real-Time, Edge*

INVITED TALKS

Unveiling CPS Heisenbugs at Scale	*Bosch RDS Tech Colloquium*	*Oct 2024*
Leveraging WebAssembly as a Debugging Target	*Wasm Research Day*	*Jun 2024*
Leveraging WebAssembly Instrumentation	*Wasm Research Day (with T. Huang)*	*Oct 2023*
Giving the Cloud an Edge with WebAssembly	*Wasm Research Day (with T. Huang)*	*Oct 2022*

HONORS AND SCHOLARSHIPS

Charles W. and Margaret A. Tolbert Scholarship	High Merit in Engineering	*Fall '20*
Centaur Technology Scholarship	Summer 2019 Internship Package	*Fall '19*
Ray Fisher Memorial Scholarship	High Merit University-Wide	*Fall '19*
UT Austin University Honors	Exemplary GPA (4.0) standing	*Fall '17 - Spr '20*

ACADEMIC EXPERIENCE

University Teaching Assistant

Virtual Machines and Managed Runtimes	*Ben Titzer, CMU*	*Fall '24*
Distributed Embedded Systems	*Anthony Rowe, CMU*	*Fall '22*
Computer Architecture	*Yale Patt, UT*	*Fall '20*
Introduction to Computing Systems	*Yale Patt, Ramesh Yerraballi, UT*	*Fall '19, '18*
Introduction to Embedded Systems	*Jonathan Valvano, UT*	*Spr '19*

INDUSTRY EXPERIENCE

IoT Cloud and Edge Integration Intern — *Bosch Research (Pittsburgh, PA)* *Jun-Aug 2022*
Designed an edge-orchestration framework (Silverline) for real-time industrial automation

GPU Design Verification Intern — *Apple Inc. (Austin, TX)* *Jun-Aug 2020*
Memory hierarchy testing improvements (speed/coverage); UVM testbenches for M2 Graphics

CPU Design Verification Intern — *Centaur Technology Inc. (Austin, TX)* *May-Aug 2019*
Memory testing tools for x86/AVX-512 chip and live analysis of CPU exception events

Software Engineering Intern — *Qube Cinema Inc. (Chennai, India)* *Jun-Aug 2018*
RNN transfer learning for seat occupancy detection at movie theaters

Machine Learning Intern — *Lucid Imaging Pvt. Ltd. (Bangalore, India)* *Jun-Aug 2018*
Transfer learning of CNNs for polypropylene detection in cotton production lines

TECHNICAL PROJECTS

Vision-Based Localization Framework — *CMU* *Dec 2021*
Android app to localization of users on CMU campus using environment triangulation Talk | Poster

RISC-V CPU Design and ISA Extension — *UT Austin (Capstone)* *Apr 2021*
Out-of-order RISC-V CPU with custom extensions to accelerate hashsets and graph search Talk | Github

Recreating the First FPGA (XC2064) — *UT Austin* *Dec 2020*
8x8 CLB FPGA design in Structural Verilog with GUI-based bitstream generation tool Github

Cellular Automata Survey Paper — *UT Austin* *May 2020*
Local 1D pattern formation and checkability theorems in cellular automata Paper

The JASP Cellular Phone — *UT Austin (445L Class)* *Dec 2019*
Cellphone designed from scratch with call+text capability; Won 1st place in project showcase Github

RTOS Design on Bare-Metal Microcontroller — *UT Austin (445M Class)* *Apr 2020*
Fully featured with process loading, priority scheduling, FAT filesystem, and wireless RPCs Talk

Texas CreateAThon (Building Innovative Solutions) — *UT Austin* *Spr '19, Spr '18*
RecycleMe: Real-time waste segregation with offloaded CNN classification *2019* | Github
ChariIoT: Localizable chair platform with IMU-based displaced tracking *2018* | Github

Home-Unity App — *HackDFW (Fort Worth, TX)* *Feb 2019*
Ecosystem to improve food/shelter provisioning for homeless; Two 1st place awards Dev | Github

Stick Fighter Embedded System Game Design — *UT Austin (319K Class)* *Nov 2017*
Two-player fighter game (on TI μC) with custom controller hardware, music, and graphics Github